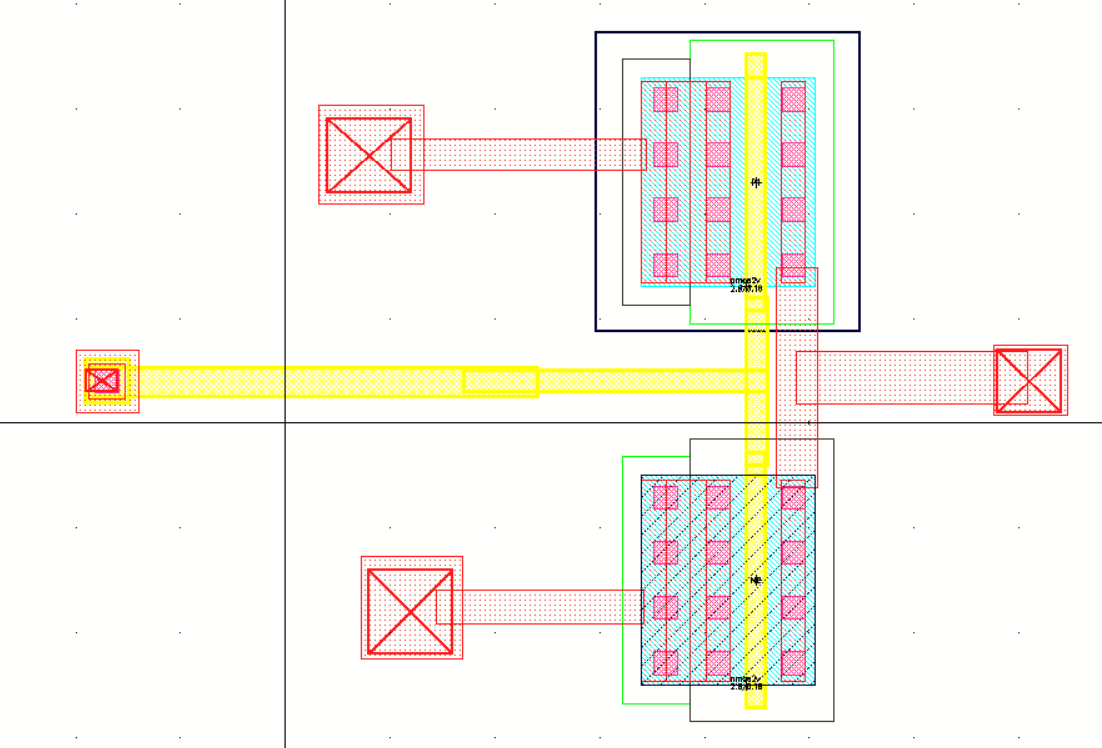
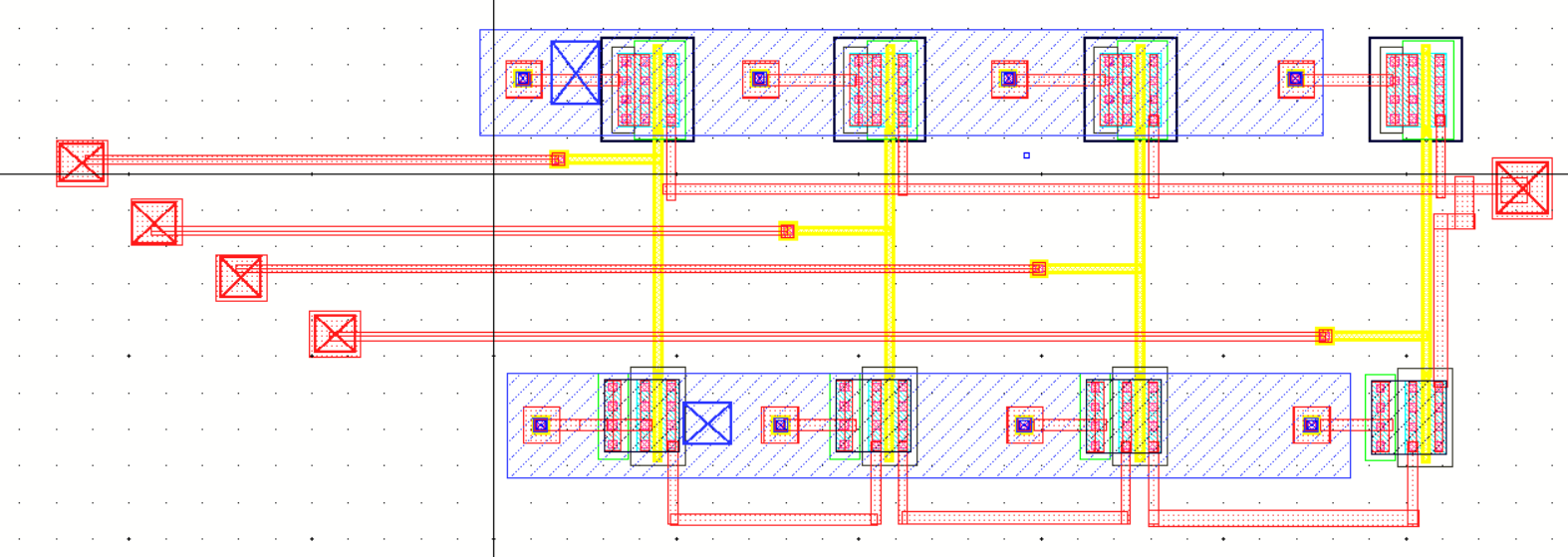
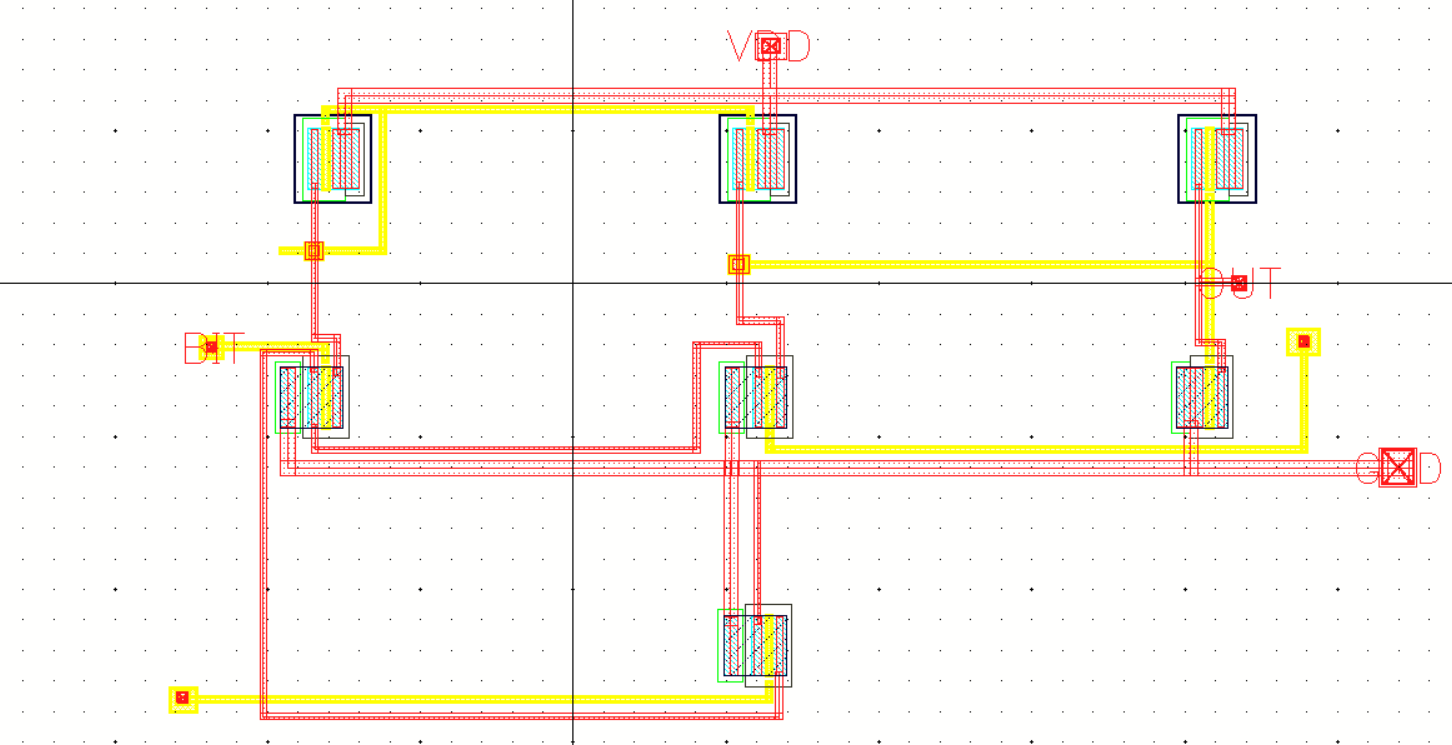
**Layout Screenshots**

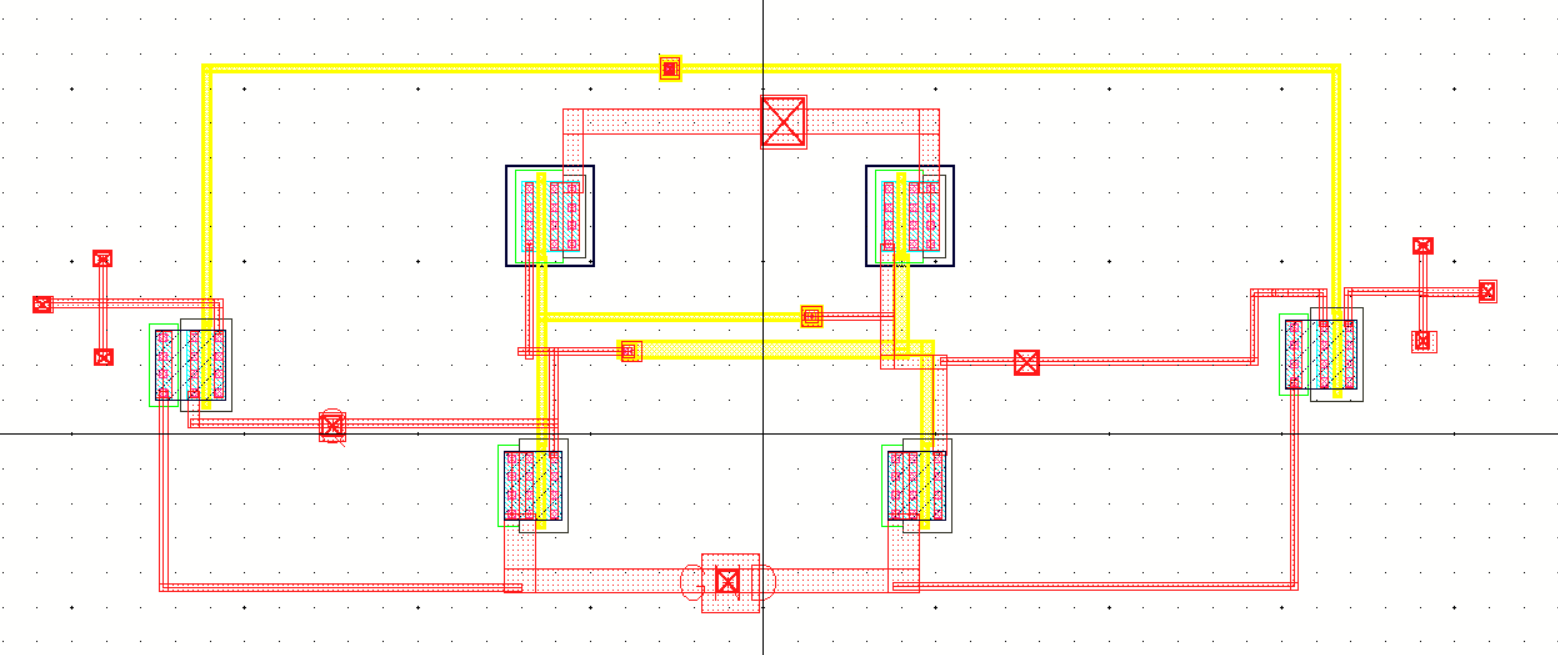
Inverter  


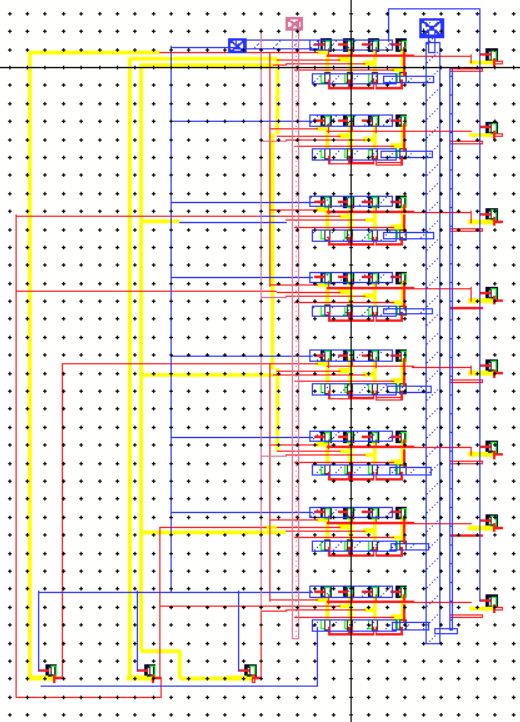
NAND4

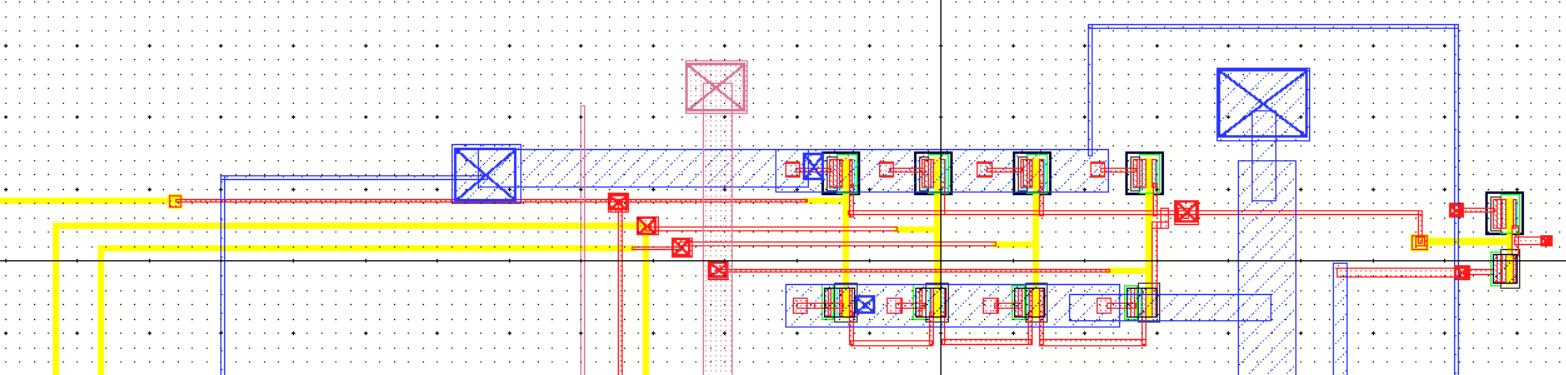


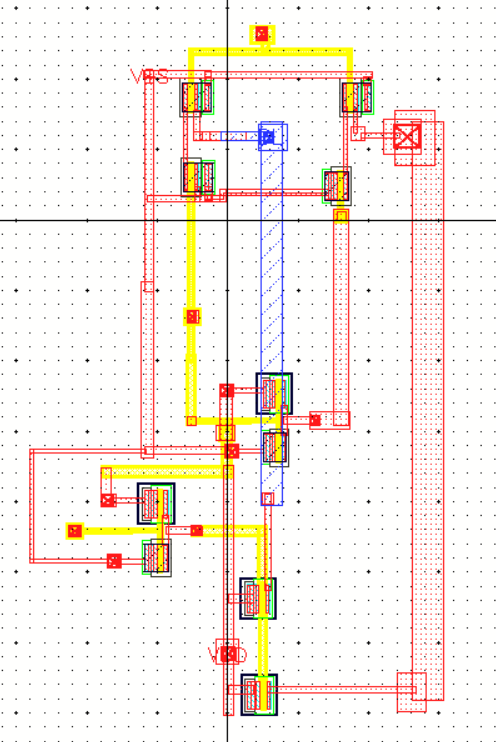
Sense Amplifier



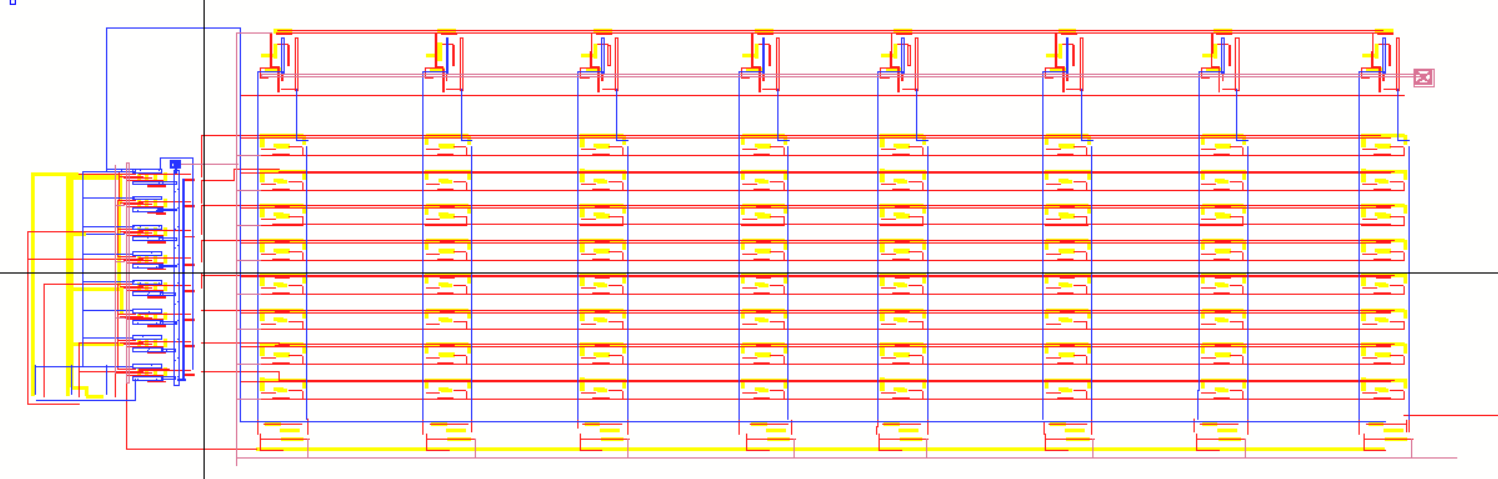
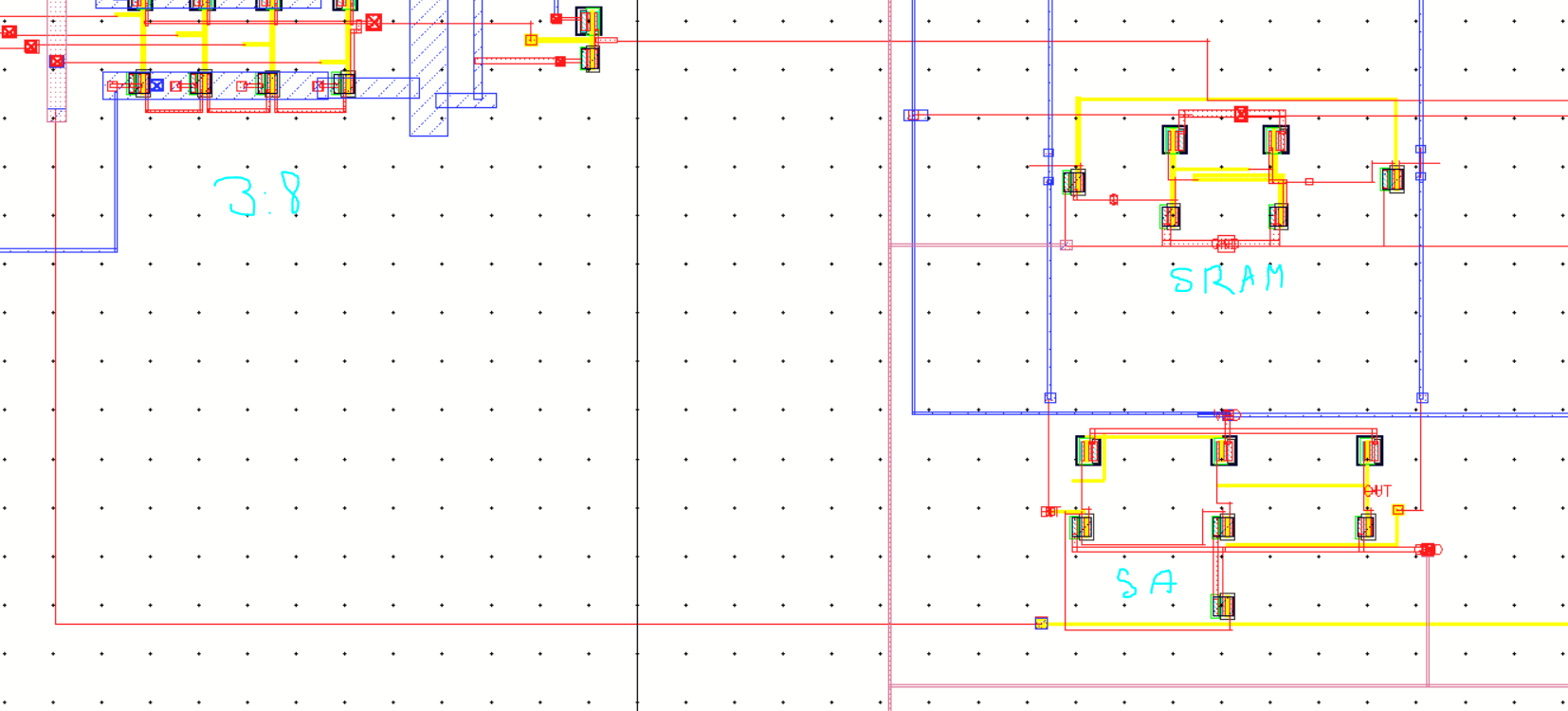
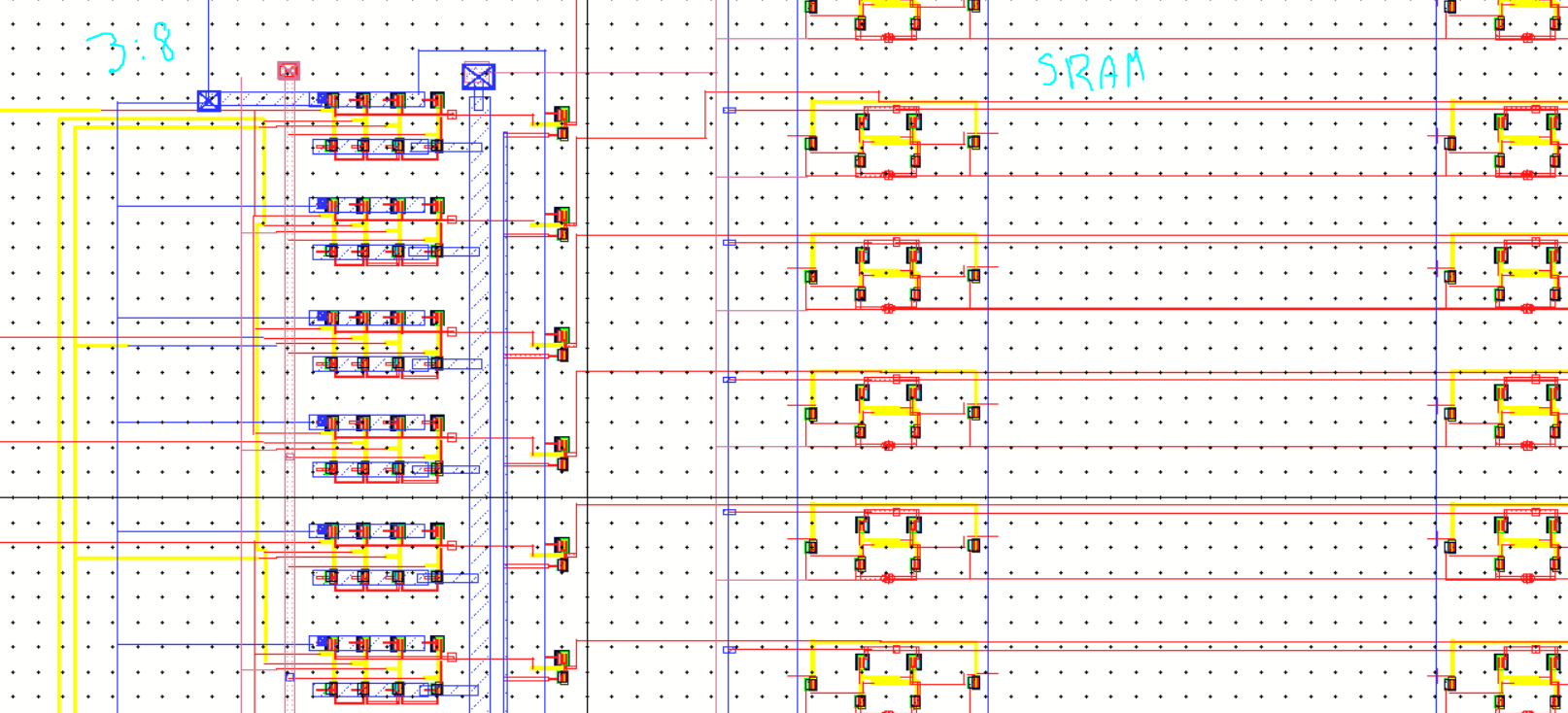
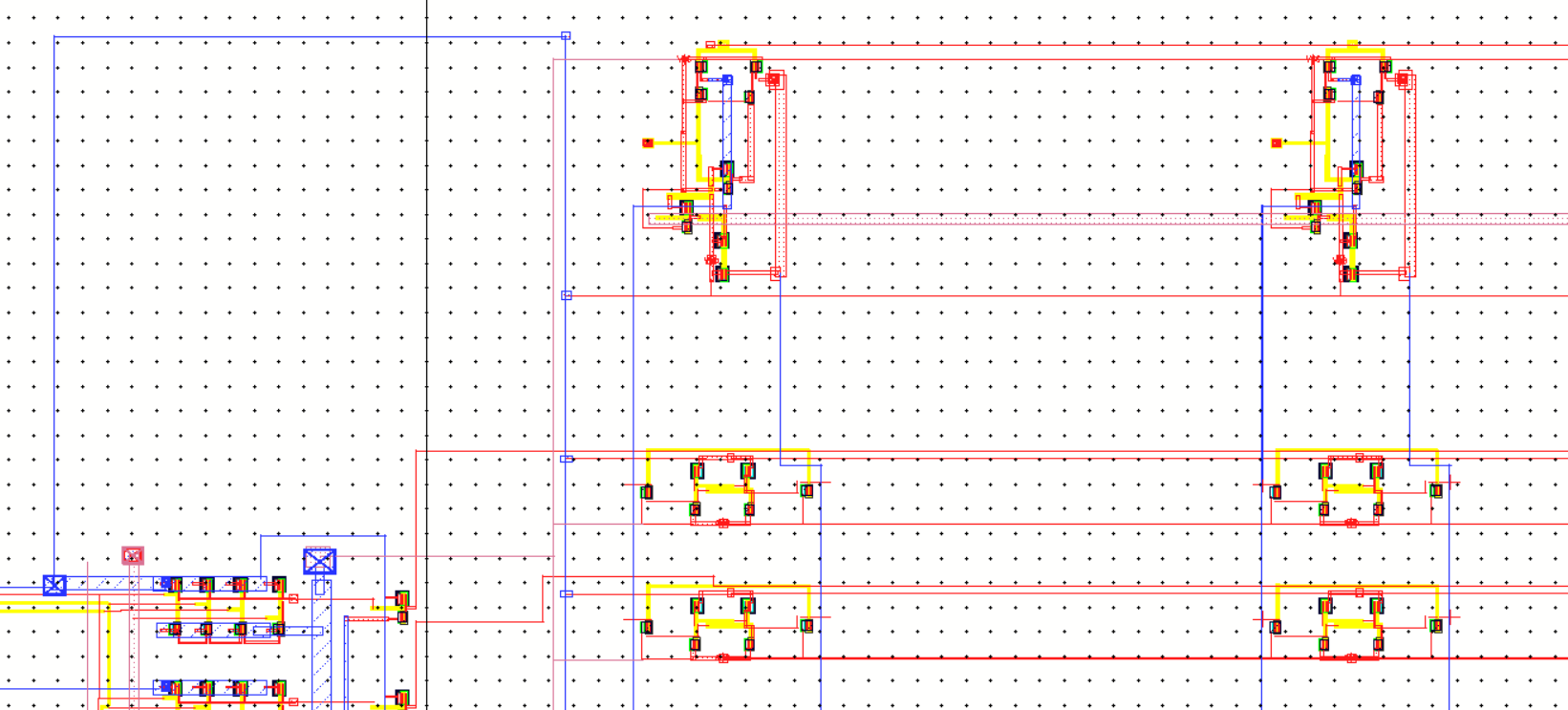
SRAM Cell  


Decoder  




Bitline Drivers  


Final Layout

Schematic  
